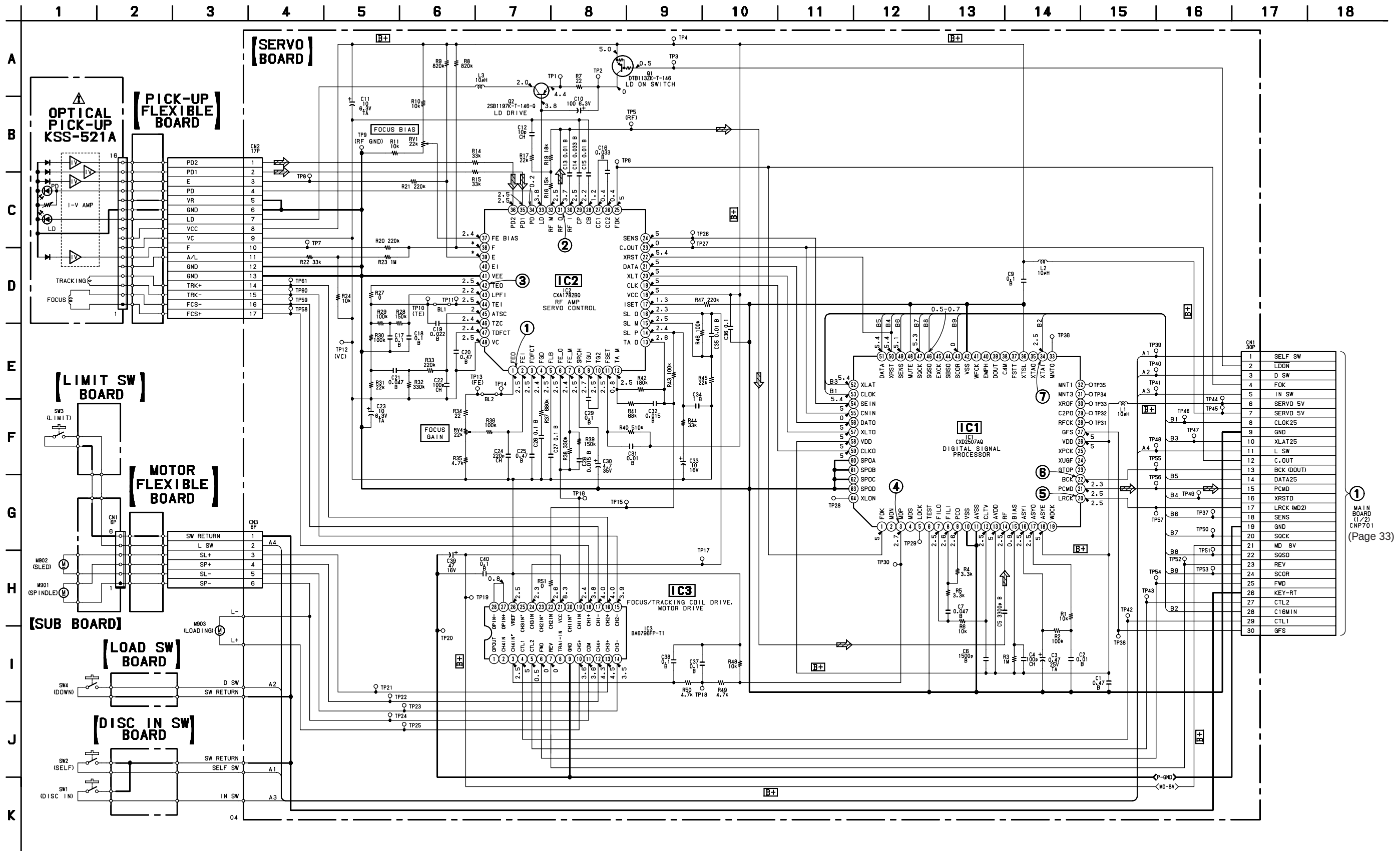
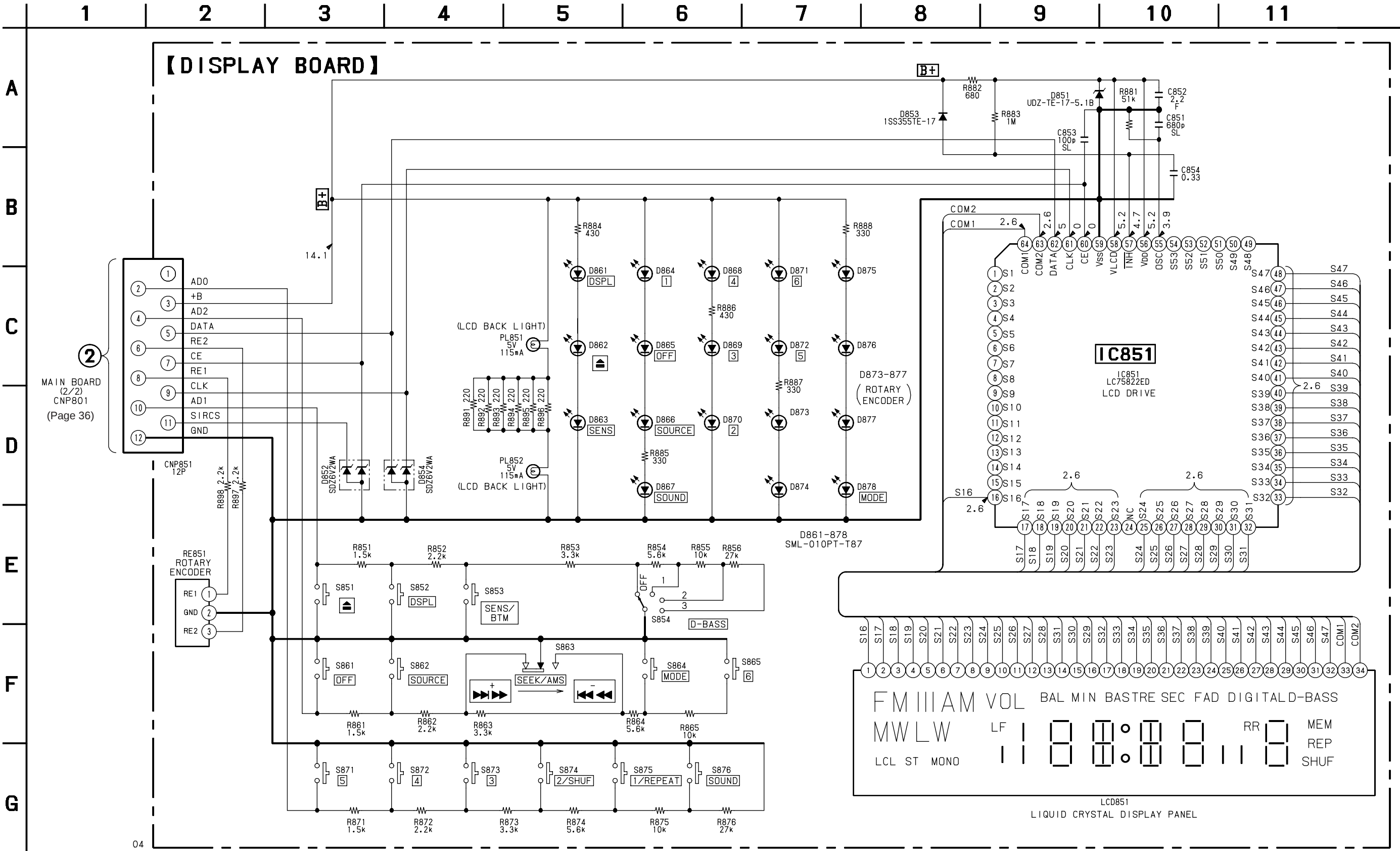


4-4. SCHEMATIC DIAGRAM — CD MECHANISM SECTION — • Refer to page 22 for Waveforms and Note and page 37 for IC Block Diagrams.



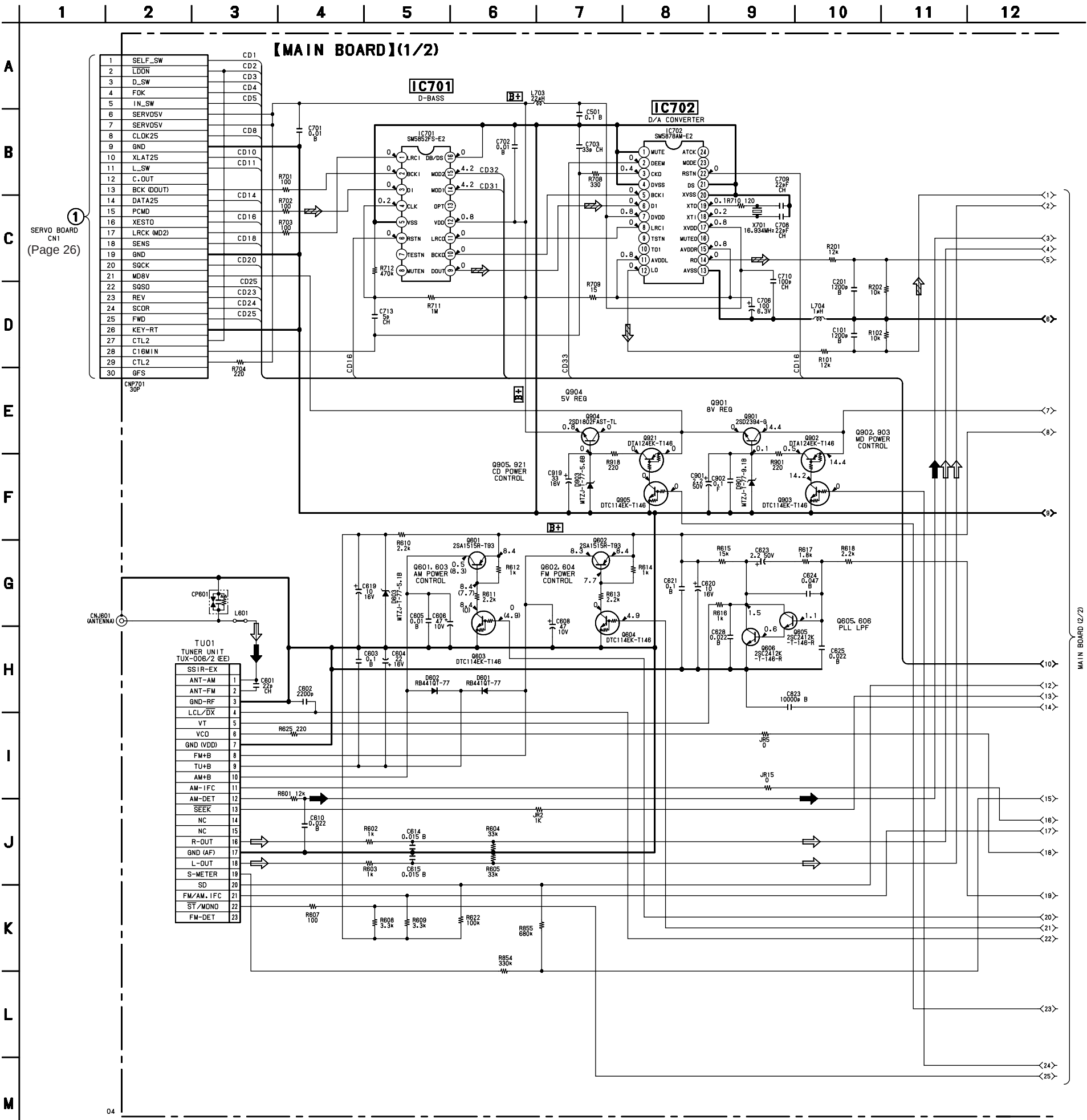
4-6. SCHEMATIC DIAGRAM — DISPLAY SECTION —



Note:

- All capacitors are in μF unless otherwise noted. pF: μpF 50 WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and $\frac{1}{4}\text{W}$ or less unless otherwise specified.
- **B+** : B+ Line.
- Power voltage is dc 14.4V and fed with regulated dc power supply from ACC and BATT cords.
- **[]** : panel designation.
- Voltage is dc with respect to ground under no-signal (detuned) condition. no mark : FM
- Voltages are taken with a VOM (Input impedance 10 M Ω). Voltage variations may be noted due to normal production tolerances.

4-8. SCHEMATIC DIAGRAM — MAIN SECTION (1/2) — • Refer to page 39 for IC Block Diagrams.

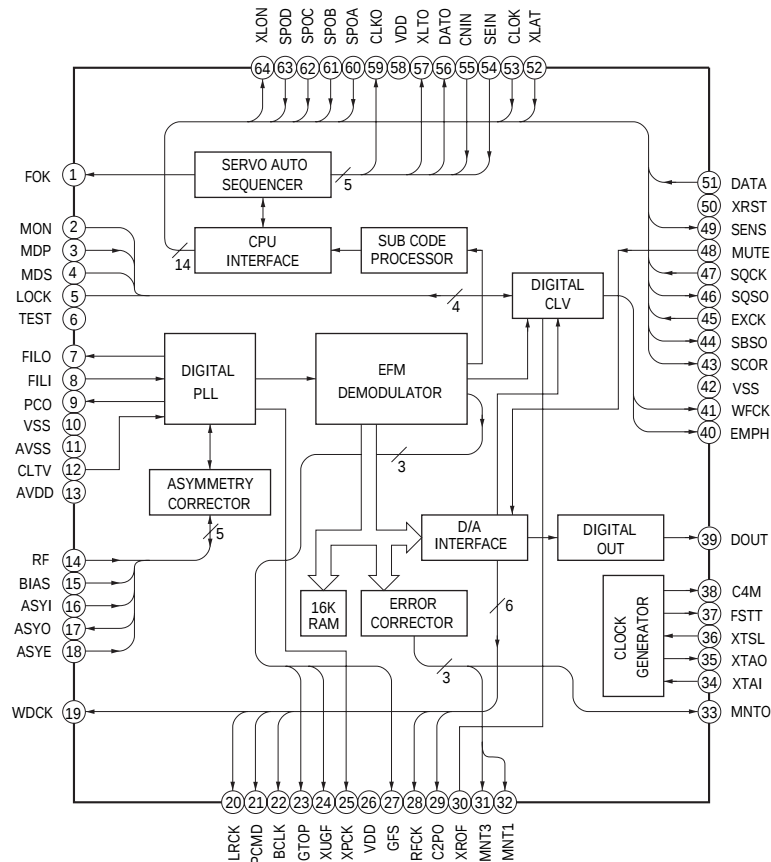


- Note:**
- All capacitors are in μF unless otherwise noted. pF: μpF 50 WV or less are not indicated except for electrolytics and tantalums.
 - All resistors are in Ω and $\frac{1}{4}\text{W}$ or less unless otherwise specified.
 - Δ : internal component.
 - $\square$: panel designation.
 - $\boxed{B+}$: B+ Line.
 - Power voltage is dc 14.4V and fed with regulated dc power supply from ACC and BATT cords.
 - Voltage is dc with respect to ground under no-signal (detuned) condition.
no mark : FM
() : MW
 - Voltages are taken with a VOM (Input impedance 10 M Ω). Voltage variations may be noted due to normal production tolerances.
 - Signal path.
 $\Rightarrow$: FM
 $\Rightarrow$: MW
 $\Rightarrow$: CD

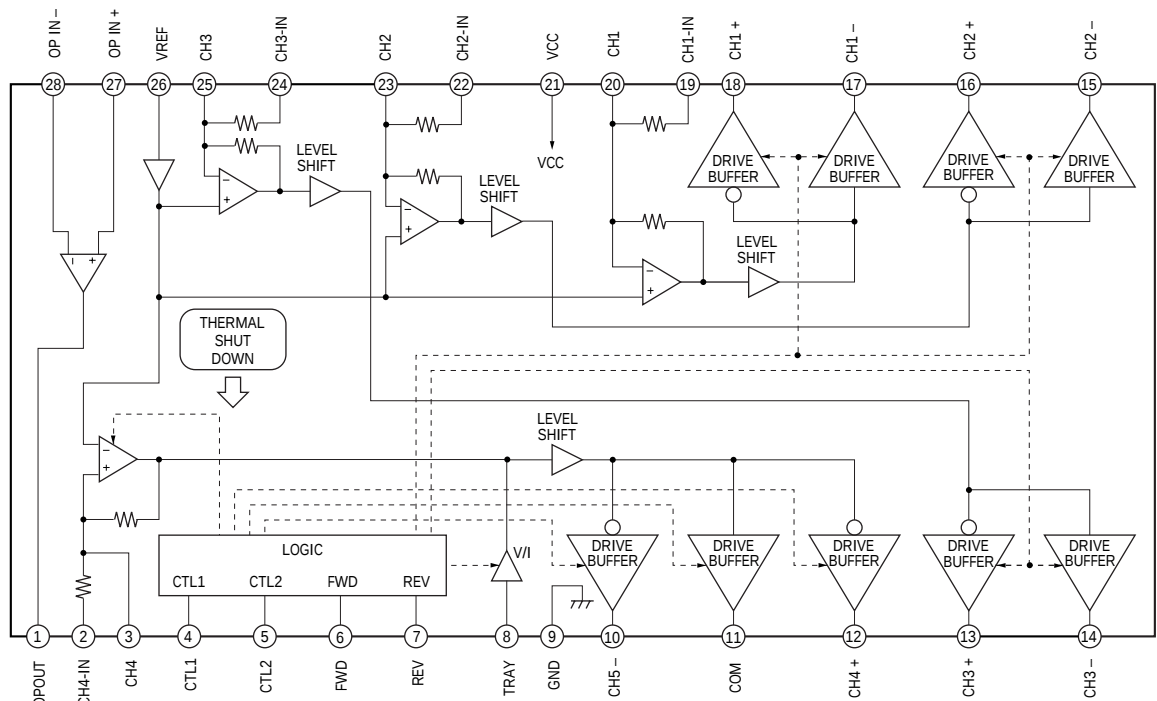


• IC Block Diagrams

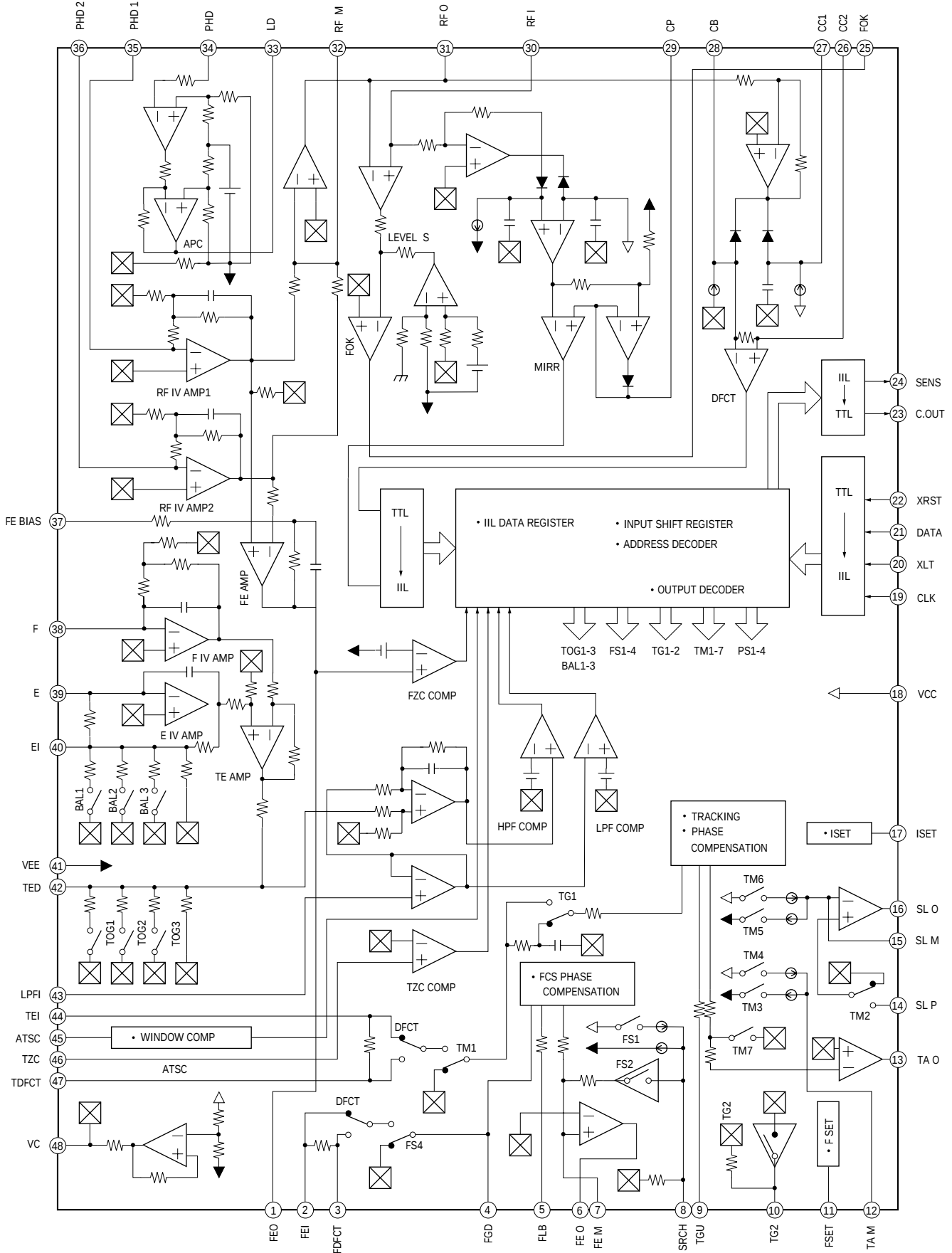
IC1 CXD2507AQ



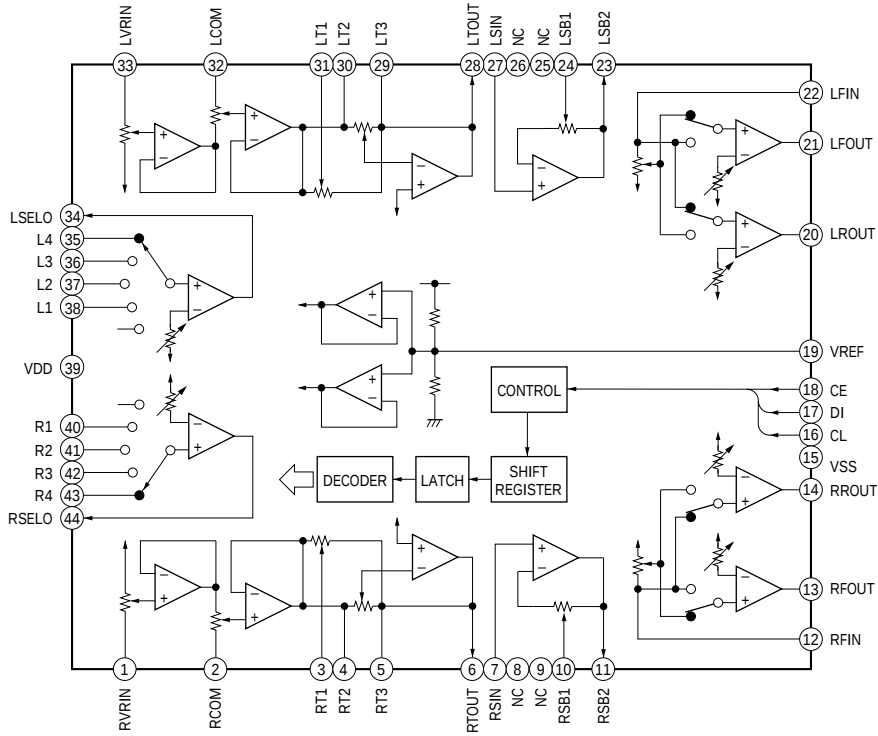
IC3 BA6796FP-T1



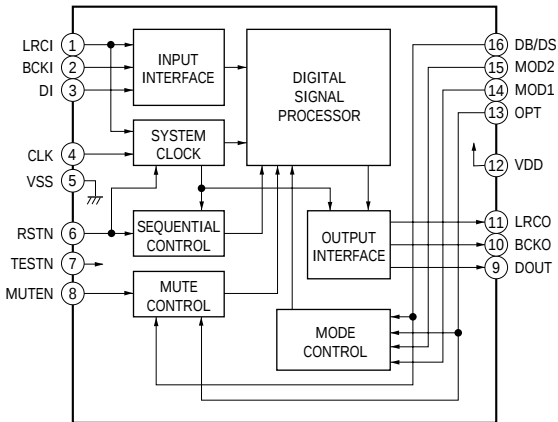
IC2 CXA1782BQ



IC401 LC75374E



IC701 SM5852FS-E2



IC702 SM5878AM-E2

